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Nyland, L.S.; Prins, J.F.; Goldberg, A.; Mills, P.H.;

Software Engineering, IEEE Transactions on , Volume: 26 , Issue: 4 , April 2000  
Pages:293 - 314
[\[Abstract\]](#)   [\[PDF Full-Text \(1600 KB\)\]](#)   IEEE JNL

## 2 An empirical three-dimensional crossover capacitance model for multilevel interconnect VLSI circuits

Shyh-Chyi Wong; Trent Gwo-Yann Lee; Dye-Junn Ma; Chuan-Jane Chao;

Semiconductor Manufacturing, IEEE Transactions on , Volume: 13 , Issue: 2 , 2000

Pages:219 - 227

[\[Abstract\]](#)   [\[PDF Full-Text \(344 KB\)\]](#)   IEEE JNL

## 3 Application of the SEPA methodology and tool suite to the National Cancer Institute

Barber, K.S.; Graser, T.J.; Jernigan, S.R.; McGiverin, B.J.; Silva, J.;

System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii International Conference on , Volume: Track4 , 5-8 Jan. 1999

Pages:15 pp.

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#### 1 Heavy tails, generalized coding, and optimal Web layout

*Xiaoyun Zhu; Jie Yu; Doyle, J.;*

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#### 2 Replacement policies for a proxy cache

*Rizzo, L.; Vicisano, L.;*

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 Pages:158 - 170

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#### 3 Modeling long distance dependence in language: topic mixtures versus dynamic cache models

*Iyer, R.M.; Ostendorf, M.;*

 Speech and Audio Processing, IEEE Transactions on , Volume: 7 , Issue: 1 , Jan 1999  
 Pages:30 - 39

[\[Abstract\]](#)    [\[PDF Full-Text \(284 KB\)\]](#)    **IEEE JNL**

#### 4 Internet routing instability

*Labovitz, C.; Malan, G.R.; Jahanian, F.;*

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**5 Device scaling limits of Si MOSFETs and their application dependenc**

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*Kong, K.; Ghosal, D.;*

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**wireless systems***Ercetin, O.; Tassiulas, L.;*

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[\[Abstract\]](#) [\[PDF Full-Text \(368 KB\)\]](#) **IEEE JNL****12 Streaming video over the Internet: approaches and directions***Dapeng Wu; Hou, Y.T.; Wenwu Zhu; Ya-Qin Zhang; Peha, J.M.;*

Circuits and Systems for Video Technology, IEEE Transactions on , Volume: 11 , Issue: 3 , March 2001

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[\[Abstract\]](#) [\[PDF Full-Text \(260 KB\)\]](#) **IEEE JNL****13 Difficulties in simulating the Internet***Floyd, S.; Paxson, V.;*

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[\[Abstract\]](#) [\[PDF Full-Text \(116 KB\)\]](#) **IEEE JNL****14 Subject index**

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[\[Abstract\]](#) [\[PDF Full-Text \(332 KB\)\]](#) **IEEE JNL****16 Trajectory sampling for direct traffic observation***Duffield, N.G.; Grossglauser, M.;*

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21 **Robust decision tree state tying for continuous speech recognition**

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22 **Taxonomy and design analysis for distributed Web caching**

*Dykes, S.G.; Jeffery, C.L.; Das, S.;*  
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23 **Adaptive resource management for flow-based IP/ATM hybrid switching systems**

*Hao Che; San-qi Li; Lin, A.;*  
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25 **Two decades of statistical language modeling: where do we go from here?**

*Rosenfeld, R.;*  
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Pages:1270 - 1278

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26 **Video staging: a proxy-server-based approach to end-to-end video delivery over wide-area networks**

*Zhi-Li Zhang; Yuewei Wang; Du, D.H.C.; Dongli Su;*  
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*Feldmann, A.; Rexford, J.; Caceres, R.;*  
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29 **Parametric design synthesis of distributed embedded systems**

*Dong-In Kang; Gerber, R.; Saksena, M.;*  
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30 **Network visualization with Nam, the VINT network animator**

*Estrin, D.; Handley, M.; Heidemann, J.; McCanne, S.; Ya Xu; Yu, H.;*  
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31 **A framework for computer performance evaluation using benchmark sets**

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34 **Speculative precomputation: long-range prefetching of delinquent I**  
*Collins, J.D.; Hong Wang; Tullsen, D.M.; Hughes, C.; Yong-Fong Lee; Lavery,  
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37 **An efficient computation-constrained block-based motion estimatio**  
**algorithm for low bit rate video coding**  
*Gallant, M.; Cote, G.; Kossentini, F.;*  
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41 **Energy-efficient design of battery-powered embedded systems**

*Simunic, T.; Benini, L.; De Micheli, G.;*

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*Kian-Lee Tan; Cheng Hian Goh; Beng Chin Ooi;*

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43 **Priority queues and sorting methods for parallel simulation**

*Grammatikakis, M.D.; Liesche, S.;*

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45 **Rapid profiling via stratified sampling**

*Subramanya Sastry, S.; Bodik, R.; Smith, J.E.;*

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46 **IEEE INFOCOM 2001 - The Conference on Computer Communication  
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49 **A cache architecture for modernizing the Usenet infrastructure**  
*Gschwind, T.; Hauswirth, M.;*

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50 **IEEE standard for integrated circuit (IC) delay and power calculatic  
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